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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	
				Filing Date	08/04/2000
				First Named Inventor	Seungug Koh
				Group Art Unit	
				Examiner Name	
Sheet	1	of	4	Attorney Docket Number	

U.S. PATENT DOCUMENTS

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

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Sheet

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4

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Application Number	
Filing Date	08/04/2000
First Named Inventor	Seungguk Koh
Group Art Unit	
Examiner Name	
Attorney Docket Number	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS		
Examiner Initials'	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	1	Martin Hoffman et al., "Optical Fiber Switches Based on Full Wafer Silicon Micromachining," Proceeding:MME 98-Micromechanics Europe, Ulvik, Norway, pp. 96499, 1998
	2	G. Marxer et al., "Reflective Modulators and By-Pass-Switches: two MEMS Components for Fiber Optic Communication," 1998 IEEE Summer Topical Meetings, Monterey, CA, pp. 27-28, 1998
	3	Q. Lai et al., "Low-Power Compact 2X2 Thermooptic Silica-on-Silicon Waveguide Switch with Fast Response," IEEE Photonics Technology Letters, Vol. 10, No. 5, pp. 681-683, May 1998
	4	R. Moosburger et al., "4X4 Digital Optical Matrix Switch Using Polymeric Oversized Rib Waveguides," IEEE Photonics Technology Letters (PTL), Vol. 10, No. 5, pp. 684-686, May 1998
	5	C.G.P. Herben et al., "A Compact Integrated InP-Based Single-Phasar Optical Crossconnect," IEEE PTL, Vol. 10, No. 5, pp. 678-680, May 1998
	6	Takashi Goh et al., "High-Extinction Ratio and Low-Loss Silica-Based 8X8 Thermooptic Matrix Switch," IEEE PTL, Vol. 10, No. 3, pp. 358-360, March 1998
	7	Takashi Goh et al., "Low-Loss and High-Extinction Ratio Silica-Based Strictly Nonblocking 16X16 Thermooptic Matrix Switch," IEEE PTL, Vol. 10, No. 6, pp. 810-812, June 1998
	8	Jane M. Simmons et al., "Optical Crossconnects of Reduced Complexity for WDM Networks with Bidirectional Symmetry," IEEE PTL, Vol. 10, No. 6, pp. 819-821, June 1998
	9	Chuan Pu et al., "Surface Micromachined Integrated Optic Polarization Beam Splitter," IEEE PTL, Vol. 10, No. 7, pp 988-990 July 1998
	10	B. Barber et al., "A Fiber Connectorized MEMS Variable Optical Attenuator," IEEE PTL, Vol. 10, No. 9, pp. 1262-1264, September 1998
	11	L.Y. Lin et al., "High-Density Micromachined Polygon Optical Crossconnects Exploiting Network Connection Symmetry," IEEE PTL, Vol. 10, No. 10, pp. 1425-1427, October 1998

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	
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Sheet	3	of	4	First Named Inventor	Seungug Koh
				Group Art Unit	
				Examiner Name	
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
	12	Cronel Marxer et al., "A Variable Optical Attenuator Based on Silicon Micromechanics," IEEE PTL, Vol. 11, No. 2, pp. 233-235, February 1999			
	13	Neil A. Jackman et al., "Optical Cross Connects for Optical Networking," Bell Labs Technical Journal, pp. 262-281, January-March 1999			
	14	D. Bruce Buchholz et al., "Broadband Fiber Access: A Fiber-to-the-Customer Access Architecture," Bell Labs Technical Journal, pp. 282-299, January-March 1999			
	15	C. Randy Giles et al., "The Wavelength Add/Drop Multiplexer for Lightwave Communication Networks," Bell Labs Technical Journal, pp. 207-229, January-March 1999			
	16	Masayuki Okuno et al., "Silica-Based 8X8 Optical Matrix Switch Integrating New Switching Units with Large Fabrication Tolerance," IEEE/OSA JLT, Vol. 143, No. 5, pp. 771-779, May 1999			
	17	P. Li et al., "Silica-based optical integrated circuits," IEE Proceedings-Optoelectron, Vol. 143, No. 5, pp. 263-280, October 1996			
	18	M. Kawachi, "Recent progress in silica-based planar lightwave circuits on silicon," IEE Proceedings-Optoelectron, Vol. 143, No. 5, pp. 257-262, October 1996			
	19	C.A. Jones et al., "Hybrid Integration onto silicon motherboards with planar silica waveguides," IEE Proceedings-Optoelectron, Vol. 143, NO. 5, October 1996			
	20	Cornel Marxer et al., "Vertical Mirrors Fabricated by Deep Reactive Ion Etching for fiber-Optic Switching Applications," IEEE/ASME Journal of MEMS, Vol. 6, No. 3, pp. 277-284, September 1997			
	21	Wen-Han Huan et al., "Released Si Microstructures Fabricated by Deep Etching and Shallow Diffusion," IEEE/ASME Journal of MEMS, Vol. 5, No. 1, pp. 18-23, March 1996			
	22	Wen-Han Juan et al., "High-Aspect-Ratio Si Vertical Micromirror Arrays for Optical Switching," IEEE/ASME journal of MEMS, Vol. 7, No. 2, pp. 207-213, June 1998			

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Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/635,043	KOH ET AL.	
Examiner Alessandro V. Amari		Art Unit 2872	Page 1 of 1	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,363,183	03-2002	Koh	385/19
	B	US-6,195,478	02-2001	Fouquet	385/17
	C	US-6,169,827	01-2001	Holman et al.	385/22
	D	US-5,144,637	09-1992	Koch et al.	372/50
	E	US-			
	F	US-			
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	J	US-			
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	Examiner Alessandro V. Amari	Art Unit 2872	Page 1 of 1

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A	US-5,024,500	06-1991	Stanley et al.	385/16
B	US-5,867,617	02-1999	Pan et al.	385/18
C	US-6,219,472	04-2001	Horino et al.	385/16
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